High-Field Electron Drift Velocity in InGaAs Quantum Wells

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Abstract- The electric field dependences of electron mobility and drift velocity in InGaAs quantum wells (QWs) of modulation doped $Al_xIn_{1-x}As/In_yGa_{1-y}As$ and $Al_xGa_{1-x}As/In_yGa_{1-y}As$ heterostructures are reviewed. It was theoretically predicted and experimentally observed that the enhancement of the low-field mobility in the $Al_xIn_{1-x}As/In_yGa_{1-y}As$ QW up to 1.2×10^4 cm²/(V s) can be achieved by increasing the In fraction *y* in the QW and barriers up to *y* = 0.7–0.8 and by decreasing Al fraction *x* up to *x* = 0.2 in the barrier layer. The largest increase by a factor of 1.8 in the electron mobility and maximal drift velocity up to $(2-4) \times 10^7$ cm/s was achieved by inserting the InAs phonon wall into the InGaAs QW. The InAs phonon wall is a new type of a specific barrier transparent to electrons and reflected phonons. The created heterojunctions with high electron mobility and drift velocity were proposed and tested as basic elements for high-speed electronic devices in the 0.5–1.0 THz frequency range.

Keywords- Heterostructures; AlInAs/InGaAs; AlGaAs/InGaAs; Microwave and THz Electronics; High-Field Electron Drift Velocity in QWs; Electron–Phonon Scattering in QWs

I. INTRODUCTION

Semiconductor heterostructures have become the basic elements of contemporary semiconductor electronics. In particular, modulation-doped AlGaAs/GaAs, AlInAs/InGaAs and AlGaAs/InGaAs heterostructures are important for application in high electron mobility transistors (HEMTs), which are the basic elements for high-speed devices in microwave and terahertz (THz) frequency ranges [1-10].

In a modulation-doped structure, the spatial separation of free electrons from an impurity-doped layer reduces impurity scattering; however, it enhances electron scattering by interface (IF) phonons in thin quantum wells (QWs).

In this paper, the possibilities of enhancing the electron mobility and drift velocity saturated in high electric fields by reducing scattering of electrons by phonons are examined. Hot electron transport peculiarities in the modulation doped AlGaAs/InGaAs and AlInAs/InGaAs heterostructures are reviewed.

The following effective tools for enhancing the electron mobility and high-field drift velocity in InGaAs QWs are proposed: (1) confinement both phonons and electrons in the QW of HEMT channel; (2) decrease of electron–IF phonon interaction strength by choosing a composition of semiconductors in the heterostructures; and (3) insertion of a phonon wall (specific barrier transparent to electrons and reflected phonons) into the QW. Application of these advanced methods allows us to create heterostructures – basic elements of high-speed electronic devices for the new area of semiconductor electronics in the 0.5–1.0 THz frequency range.

II. SATURATED ELECTRON DRIFT VELOCITY IN HIGH ELECTRIC FIELDS IN AlGaAs/InGaAs AND AlInAs/InGaAs QANTUM WELLS

Saturation of electron drift velocity in high electric fields in basic semiconductors (Si, GaAs, InGaAs and others) limits the enhancement of main parameters of field effect transistors: the maximum cutoff frequency and gain.

Fig. 1 shows the typical electric field dependences of electron drift velocity, $v_{dr}(E)$, in bulk GaAs and InAs, as well as in In_{0.2}Ga_{0.8}As and In_{0.5}Ga_{0.5}As alloys. One can see that the drift velocity in bulk In_yGa_{1-y}As alloys are saturated in higher electric fields at 1×10^7 cm/s in spite of a large increase in low-field mobility and maximal drift velocity $v_{max}(E_{th})$ at the threshold field E_{th} as a mole fraction y of In increases in the In_yGa_{1-y}As. It should be noted that $E_{th} \approx 4$ kV/cm and weakly depends on the composition of In_yGa_{1-y}As.

Fig. 2 shows the experimentally measured field dependences of electron drift velocity in the GaAs QW, which radically differ from the similar dependences in the bulk GaAs. The region with a negative differential conductivity is absent. The drift velocity in GaAs QWs in high electric fields exceeds the saturated drift velocity in bulk materials, in spite of much less low-field mobility in GaAs QWs in comparison with bulk materials [11-15]. In samples A and B with a thicker QW (30 nm), the drift velocity exceeds the saturated drift velocity in the bulk GaAs by a factor of 1.5–2. The largest increase of the drift velocity is observed experimentally in sample B. This structure contains three thin (1 monolayer of InAs) polar optical (PO) phonon barriers dividing the GaAs QW into four thin phonon wells. At E = 15 kV/cm, the drift velocity in sample B reaches 2.1×10^7 cm/s.



Fig. 1 Field dependences of drift velocity of electrons in the bulk $In_yGa_{1-y}As$ solid solution at y=0, 0.2, 0.5, and 1



Fig. 2 Field dependences of the electron drift velocity $v_{dr}(E)$ in the doublebarrier Al_{0.3}Ga_{0.7}As/GaAs/Al_{0.3}Ga_{0.7}As structures with a different thickness of the GaAs QW: 30 nm in samples A and B and 10 nm in sample C. Sample B has three thin (1 monolayer of InAs) phonon barriers inserted into the GaAs layer. The full curve represents $v_{dr}(E)$ in the bulk GaAs

However, in sample C with the thin QW (10 nm), the drift velocity at electric fields E > 6 kV/cm saturates and does not exceed the velocity in the bulk GaAs. This is explained by a strong increase of electron scattering rate (SR) by IF phonons in the thin (<10 nm) QW [16].

The field dependences of the electron drift velocity $v_{dr}(E)$ in the QWs calculated using Monte Carlo simulations at various fractions of Al in the QW barriers for the Al_xIn_{1-x}As/In_{0.5}Ga_{0.5}As/Al_xIn_{1-x}As structure are shown in Fig. 3(a) [17, 18]. The $v_{dr}(E)$ in the QWs was calculated taking into account the scattering of electrons only by PO and IF phonons confined in the QW. The In_{0.5}Ga_{0.5}As QW thickness is accepted as 16 nm.

The field dependences of the occupancy of Γ and *L* valleys in the QWs of the corresponding compositions are shown in Fig. 3(b).

The most essential feature of the $v_{dr}(E)$ in the Al_xIn_{1-x}As/In_{0.5}Ga_{0.5}As QW is the effect of the QW barrier composition on the threshold field for the intervalley transfer, E_{th} , in the QW layer. The threshold field E_{th} (at which the drift velocity in the In_{0.5}Ga_{0.5}As QW is maximal) increases and the low-field mobility decreases as the *x* fraction of Al in the barrier layer increases.

The intervalley Γ -*L* transfer is responsible for the decrease of the drift velocity at $E > E_{\text{th}}$ (see Fig. 3(b)). One can see that the mobility in the sample with x = 0.2 becomes less than that with x = 0.5 at E > 7 kV/cm. Then, at E > 7 kV/cm, the drift velocity in the sample with x = 0.5 becomes greater than that with x = 0.2.



Fig. 3 Calculated field dependences of (a) drift velocity of electrons and (b) fractions of electrons n_{n_{Γ}/n_s} and n_L/n_s in Γ and L valleys, respectively, in In_{0.5}Ga_{0.5}As, for various compositions of the Al_xIn_{1-x}As barrier layer: *x*=0.2, 0.5, and 0.6

Therefore, the decrease of the fraction x of Al up to x = 0.2 in the QW barrier layers are the effective tool for enhancing the low-field electron mobility in the modulation doped In_yGa_{1-y}As/Al_xIn_{1-x}As heterostructures.

The experimental field dependences of the InGaAs QW channel conductivity at different QW barrier compositions are presented in Fig. 4. The measurements were performed on the samples in the form of gateless mesa 100- μ m-wide structures with deposited 100×100 μ m² Au/Ni/Ge ohmic contacts onto the samples. The length of the samples (a distance between ohmic contacts) was *d* = 10 μ m. The voltage pulses of 80 ns duration were used.

One can see that the current-field dependences are sub-linear. The threshold field E_{th} for current saturation and instabilities depends on the QW barrier composition and changes from 2 to 7 kV/cm. Note that the E_{th} =2.5 kV/cm is significantly less than that E_{th} =4 kV/cm in bulk materials.



Fig. 4 Field dependences of the current in the InGaAs QW channel for various types of QW barrier layers. Arrows show the threshold field of current instabilities

Therefore, the experimental data confirm the predicted excess of the electron drift velocity in the moderately thick GaAs QW over the maximum saturated drift velocity in a bulk GaAs.

III. ENHANCEMENT OF ELECTRON MOBILITY IN InGaAs QANTUM WELLS BY CHANGING COMPOSITION OF Al, In, Gai, As

AND Al_xGa_{1-x}As/In_yGa_{1-y}As HETEROSTRUCTURES

Let us consider the possibilities to increase the electron mobility and high-field drift velocity in the modulation doped AlInAs/InGaAs and AlGaAs/InGaAs QW channels by tuning the electron scattering by IF phonons by choosing the composition of barriers in $Al_xIn_{x}As/In_vGa_{1-v}As$ and $Al_xGa_{1-v}As$ heterostructures.

Electron–IF phonon scattering in Al_xIn_{1-x}As/In_yGa_{1-y}As heterostructures has been investigated in [11, 17–22]. A scattering rate of an electron (with the initial state wave vector \mathbf{k}_i) by ν -mode PO and IF phonons confined in the QW of thickness L_n can be estimated as [11]:

$$W_{\nu}(k_{i},L_{n}) = WO \int_{0}^{2\pi} \int_{-L_{n}/2}^{|+L_{n}/2} \varphi_{ei} \varphi_{q\nu} \varphi_{ef} dz \Big|^{2} F_{\nu}^{2} d\theta, \qquad (1)$$

where φ_{ei} and φ_{ef} are the initial and final normalized electron wave functions; φ_{qv} is the phonon potential envelope function; F_v^2 is the factor determined by the interaction force of electrons with PO and IF phonons; and $WO = me^2/(\pi\hbar^3)(N_{qv} \pm 1/2 + 1/2)$, where *m* and *e* are the electron mass and charge, respectively, N_{qv} is the *v*-mode phonon number, and **f**_i is the Planck's constant.

According to the dielectric continuum model, the factor F_{ν}^2 for confined PO phonons with the frequency $\omega_{\rm L}$ is equal to

$$F_{\rm PO}^2 = \frac{\hbar\omega_L}{2} \left(\frac{1}{\chi_{\infty}} - \frac{1}{\chi_{\rm st}} \right),\tag{2}$$

and for confined IF phonons with frequency $\omega_{\rm IF}$,

$$F_{\rm IF}^{2}(\omega_{\rm IF}) = \left[\varepsilon' \int_{-L_{\rm A}/2}^{L_{\rm A}/2} (q^{2} \varphi_{q}^{2} + \varphi_{q}'^{2}) dz\right]^{-1} \quad \text{with} \quad \varepsilon' = \frac{2\omega_{\rm IF}(\omega_{\rm L}^{2} - \omega_{\rm T}^{2})}{(\omega_{\rm IF}^{2} - \omega_{\rm T}^{2})^{2}},\tag{3}$$

where χ_{∞} and χ_{st} are the optical and static dielectric constants, respectively, ω_L and ω_T are the longitudinal and transverse optical phonon frequencies, respectively [19].



Fig. 5 Dependences of the interaction force of electrons with IF phonons with the frequency ω_{IF} , $F_{IF}^2(\omega_{IF})$, on fractions *x* of Al in the composition of barriers and *y* of In in the composition of the QW for the (a) Al_xIn_{1-x}As/In_yGa_{1-y}As and (b) Al_xGa_{1-x}As/In_{0.2}Ga_{0.8}As heterostructures

The frequency ω_{IF} and, therefore, the factor $F_{\text{IF}}^2(\omega_{\text{IF}})$ are determined by the composition of semiconductors forming the interface. The dependence of the factor $F_{\text{IF}}^2(\omega_{\text{IF}})$ on the fraction x of Al in the Al_xIn_{1-x}As barrier for several values of the fraction y of In in the In_yGa_{1-y}As QW is shown in Fig. 5(a). It can be seen that the factor $F_{\text{IF}}^2(\omega_{\text{IF}})$ increases as the fraction x of Al in the Al_xIn_{1-x}As and Al_xGa_{1-x}As barriers increases. The variation in the fraction x of Al in the barrier allows us to vary the electron SR by confined IF phonons in QWs by a factor of tens. The smaller value of $F_{\text{IF}}^2(\omega_{\text{IF}})$ corresponds to the larger value of the electron mobility. In the Al_xIn_{1-x}As/In_{0.5}Ga_{0.5}As structure, the factor $F_{\text{IF}}^2(\omega_{\text{IF}})$ at x=0.5 is seven times larger in comparison with that at x=0.2. Accordingly, the calculated low-field electron mobility at x=0.2 is three times larger in comparison with that at x=0.6 (see Fig. 3).

In the Al_xGa_{1-x}As/In_{0.2}Ga_{0.8}As structure, the factor $F_{IF}^2(\omega_{IF})$ at x=0.15 is two times large than that at x=0 (see Fig. 5(b)). The experimentally measured drift velocity for the GaAs/In_{0.2}Ga_{0.8}As structures exceeds this value for the Al_{0.15}Ga_{0.85}As/In_{0.2}Ga_{0.8}As structures by a factor of 1.3 (see Fig. 4).

The increase in the electron mobility as the electron–IF phonon SR decreases was experimentally observed for four types of heterostructures: $Al_{0.15}In_{0.85}As/In_{0.2}Ga_{0.8}As$, $GaAs/In_{0.2}Ga_{0.8}As$, and $Al_{0.48}In_{0.52}As/In_{0.53}Ga_{0.47}As$ and $Al_{0.3}In_{0.7}As/In_{0.8}Ga_{0.2}As$ (Table 1).

Heterostructures	$\mu_{\rm H}, 10^3$ cm ² V ⁻¹ s ⁻¹	$n_{s0}, 10^{12}$ cm ⁻²			
Al _{0.3} In _{0.7} As/In _{0.8} Ga _{0.2} As (HEM)	12.3	1.4			
$Al_{0.48}In_{0.52}As/In_{0.53}Ga_{0.47}As$	6.2	1.0			
GaAs/In _{0.2} Ga _{0.8} As	6.4	2.0			
$Al_{0.15}Ga_{0.85}As/In_{0.2}Ga_{0.8}As$	5.0	2.9			

TABLE 1 HALL MOBILITY μ_H and sheet electron density n_{s0} at 300 K in the samples with different In and Al fractions in the QW and BARRIERS

The experimental measurements of electron Hall mobility in the grown novel $Al_{0.3}In_{0.7}As/In_{0.8}Ga_{0.2}As$ and the conventional modulation doped $Al_{0.48}In_{0.52}$ As/In_{0.53}Ga_{0.47}As heterostructures confirm this large increase in the electron mobility with the increased InAs content in the barrier and QW layers. The measured electron mobility and sheet electron density in the conventional modulation doped $Al_{0.48}In_{0.52}$ As/In_{0.53}Ga_{0.47}As structures were $\mu_{\rm H} = 6.2 \times 10^3$ cm²V⁻¹s⁻¹ and $n_{s0} = 1.0 \times 10^{12}$ cm⁻² at room temperature. Meanwhile, in the novel $Al_{0.3}In_{0.7}As/In_{0.8}Ga_{0.2}As$ structures (named as high electron mobility (HEM) structure), $\mu_{\rm H}$ and n_{s0} reach the values of 12.3×10^3 cm²V⁻¹s⁻¹ and 1.4×10^{12} cm⁻², respectively. It is worth to note that at 77 K, the electron mobility and density in HEM structures were $\mu_{\rm H} = 50.5 \times 10^3$ cm²V⁻¹s⁻¹ and $n_{s0} = 1.3 \times 10^{12}$ cm⁻² [20, 24].

Thus, for the first time, the largest increase in the electron mobility in the modulation-doped AlGaAs/InGaAs and AlInAs/InGaAs QWs is obtained by choosing the composition of the both AlGaAs and AlInAs barriers and InGaAs QW layers. It should be noted that the mobility value of 12.3×10^3 cm²V⁻¹s⁻¹ achieved in the Al_{0.3}In_{0.7}As/In_{0.8}Ga_{0.2}As structure is among the best ones ever reported for AlInAs/InGaAs heterostructures ($\mu = (10-16) \times 10^3$ cm²V⁻¹s⁻¹) at 300 K [25–27].

Therefore, the increase of fraction y of In in the QW up to y = 0.7-0.8 as well as the decrease of fraction x of Al to x=0.2 in the barrier layer are the effective tool for enhancing the electron mobility in the modulation doped $Al_xIn_{1-x}As/In_yGa_{1-y}As$ heterostructure.

IV. HIGH-ELECTRIC FIELD ELECTRON TRANSPORT IN AlInAs/InGaAs QUANTUM WELLS WITH INSERTED InAs BARRIERS (PHONON WALLS)

We will consider a thin layer of InAs as a deep and thin QW in the centre of the thick (17 nm) AlInAs/InGaAs QW. It is assumed that the InAs layer does not disturb an electron wave function in AlInAs/InGaAs QW, but reflects PO and IF phonons. InAs layer is a barrier for phonons – phonon wall.

PO and IF phonon confinement in narrow phonon layers by inserting a thin InAs layer (phonon wall) into a QW of modulation doped AlInAs / InGaAs heterostructures is used to decrease the electron–phonon scattering rate and increase the electron mobility [7, 20–24].

Let us find the optimal InAs layer thickness to achieve the largest increase of low field electron mobility in the In_{0.5}Ga_{0.5}As.

Fig. 6 shows the calculated dependence of mean electron mobility in the $In_{0.5}Ga_{0.5}As$ QW on a thickness L_A of the InAs layer inserted into the QW centre. The mean electron mobility in the QW is estimated as

$$\overline{\mu}(L_A) = \frac{e}{\overline{m}(L_A)} \frac{1}{WL(L_A)},\tag{4}$$

where *m* is the mean electron effective mass in the layered heterostructure and $WL(L_A)$ is the total scattering rate *WL* of electrons with the energy of 50 meV by confined PO and IF phonons in the 17 nm-thick InGaAs QW. One can see the alternating change of the mobility in the QW dependently on the InAs insert thickness.



Fig. 6 Estimated dependences of the mean mobility $\overline{\mu}$ of electrons having the energy of 50 meV on the thickness L_A of the inserted InAs layer into the 17-nm In_{0.5}Ga_{0.5}As QW. The black dot shows the mobility in the In_{0.5}Ga_{0.5}As QW in the absence of the InAs layer

The competition between the SR increase and the effective mass decrease determines the alternating dependence of the mobility on the inserted InAs layer thickness L_A . At $L_A > 5$ nm, the electron mobility decreases because of an increase in the electron–phonon SR in the InAs layer in spite of the increase in electron population with lower effective mass in the wider InAs layer. One can see that the mean electron mobility $\overline{\mu}$ increases to the maximal value at $L_A = 4$ nm, and it exceeds the mobility in the structures without the InAs insert by a factor of 1.8.

Five types of the modulation doped double barrier $In_{0.5}Al_{0.5}As/In_{0.5}As/In_{0.5}As/In_{0.5}As$ heterostructures with the InAs layer of different thickness inserted into the center of 17-nm-thick InGaAs QW were grown using molecular beam epitaxy. The structures were grown on InP substrates.

Table 2 shows the experimental data of low-field mobility and density of electrons in the samples of $Al_{0.5}In_{0.5}As/In_{0.5}Ga_{0.5}As/Al_{0.5}In_{0.5}As$ heterostructures with the InAs layer of different thickness inserted into the center of 17-nm-thick InGaAs QW.

It should be noted that the Hall electron mobility μ_H and density n_{sH} measured in the grown heterostructures differ from μ_{sB} and n_{sB} measured from geometrical magnetoresistance in the samples with deposited ohmic contacts. These facts are explained in [1] as a result of the change of HEMT channel conductivity due to change of the field perpendicular to the QW channel plane when the ohmic contacts are deposited.

FABLE 2 MAXIMAL DRIFT VELOCITY, THRESHOLD FIELD, ELECTRON MOBILITIES AND DENSITIES IN THE SAMPLE	es a	l _{0.5} In	0.5As/In	$_{0.5}$ Ga $_{0.5}$	5As	WITH]	[nAs	INSE	ERTS
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Sample No. In ₀₋₅ Ga ₀₋₅ As (17-nm QW)	Thickness of InAs inserts, nm	$\mu_{_H}$, 10 ³ $\mathrm{cm}^2/(\mathrm{V}\cdot\mathrm{s})$	<i>n</i> _{sH} , 10 ¹² cm ⁻²	μ_{sB} , 10 ³ cm ² /(V·s)	$n_{sB}^{}$, 10 ¹² cm ⁻²	<i>v</i> _{max} , 10⁷ cm/s	<i>E_{th}</i> , 10 ³ V/cm
861	_	8.39	2.61	3.48	0.84	1.0	12
864	3.5+3.5	1.34	2.7	1.7	1.04	2.2	17
865	4.0	9.78	3.24	11.5	1.3	2.0	3
867	4.0	9.11	2.85	9.06	1.22	2.0	5
868	4.0	3.06	3.96	4.0	2.67	2.0	7

The low-field mobility μ in samples 865 and 867 with the inserted 4-nm InAs layer reaches 10⁴ cm²/(V s) and is much higher than that in the basic sample 861 without the InAs insert. The mobility in sample 864 with the thicker (3.5 + 3.5 nm) InAs layer in the InGaAs QW is the lowest. This is in agreement with the estimation of the mobility dependence on the inserted InAs layer thickness L_A (see Fig. 6). The inserted 4-nm InAs layer in sample 868 is highly doped, while the InAs inserts in samples 865 and 867 and in all other samples are not doped. The low-field mobility in sample 868 due to high electron scattering by charged impurities is much lower than those in samples 865 and 867.

The electric field dependences of the conductivity of the InGaAs QW with the inserted InAs layer were measured on the samples in the form of gateless mesa-structures (400 μ m in width) with a deposited line of Au/Ni/Ge ohmic contacts having an area of 400×400 μ m² and spaced by distance *d* = 18 μ m.

The field dependences of the drift velocity, $v_{dr}(E)$, are shown in Fig. 7. In high electric fields, the maximal drift velocity v_{max} at the threshold field E_{th} for the electron inter-valley transfer in the samples with the InAs inserts is around 2×10^7 cm/s. For sample 861 without the InAs insert, the maximal drift velocity is 1×10^7 cm/s. It is worth noting that the threshold field E_{th} in samples with the lower mobility is much larger than those in high-mobility samples 865, 867. This means that the maximal drift velocity v_{max} in the samples with lower electron mobility μ is achieved at higher threshold electric fields E_{th} . This is in agreement with the $v_{dr}(E)$ calculations (see Fig. 3).

It is worth noting that the drift velocity in the fields of 1-4 kV/cm in the samples 865 and 867 with the inserted 4-nm InAs phonon wall in the 17-nm In_{0.5}Ga_{0.5}As QW exceeds the drift velocity in the sample 861 without the inserted 4-nm InAs phonon wall 2–5 times.

Therefore, the insertion of the InAs phonon wall into the InGaAs QW is the most effective tool for the enhancement of the electron mobility and drift velocity in InGaAs QWs.



Fig. 7 Experimental field dependences of the drift velocity, $v_{dr}(E)$, in different types of heterostructures (see Table 2)

V. CONCLUSIONS

Therefore, the theoretical and experimental investigations of electric field dependence of drift velocity in the InGaAs QW discover the following peculiarities of electron transport at high electric fields in the InGaAs channel of high-speed transistors.

- The confinement and localization of PO and IF phonons as well as electrons in QWs with thickness of 10–30 nm decrease the electron-phonon scattering rate.
- The electron–IF phonon SRs are regulated by the composition of the $Al_xIn_{1-x}As/In_yGa_{1-y}As$ heterostructures.
- The InAs layer inserted into the InGaAs QW is a new type of barrier a phonon wall transparent to electrons and reflected PO and IF phonons effectively enhances the electron mobility and drift velocity in the InGaAs QWs.

The effective tools for the great enhancement of the electron mobility and drift velocity in the InGaAs QW were proposed and realize

- The engineering of the electron-phonon interaction strength by increasing the In fraction y in the $In_yGa_{1-y}As$ QW up to y=0.7-0.8 and by decreasing Al fraction x up to x = 0.2 in the $Al_xGa_{1-x}As$ barrier layer; and
- The insertion of the InAs phonon wall into the QW. When the 4 nm-InAs layer is inserted into the 17-nm thick InGaAs QW, the mobility reaches 10⁴ cm²V⁻¹s⁻¹ and exceeds the mobility in the structure without the InAs insert by a factor of 1.8.

The realization of these tools allows us to obtain the mobility in the InGaAs/AlInAs up to $10^4 \text{ cm}^2 \text{V}^{-1} \text{s}^{-1}$ and the maximal drift velocity in high electric fields up to $(2-3)\times107$ cm/s. The created heterojunctions with the high electron mobility and drift velocity were proposed and tested as basic elements for high-speed electronic devices in the 0.5–1.0 THz frequency range.

It is worth noting that InGaAs/AlInAs heterostructures with a lower scattering rate and higher mobility and maximal drift

velocity were produced. The transistors having $f_T > 300$ GHz were made on a basis of these heterostructures in Institute of Ultrahigh Frequency Semiconductor Electronics of Russian Academy of Sciences (Moscow) [28].

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REFERENCES

- [1] J. Požela, K. Požela, V. Jucienė and A. Shkolnik, "Hot electron transport in heterostructures," *Semicond. Sci. Technol.*, vol. 26, pp. 014025-1–5, 2011.
- [2] I. S. Vasil'evskii, G. B. Galiev, E. A. Klimov, K. Požela, J. Požela, V. Jucienė, A. Sužiedėlis, N. Žurauskienė, S. Keršulis and V. Stankevič, "Semiconductors," vol. 45, pp. 1169-1172, 2011 (Engl. Transl. of Fiz. Tekh. Poluprovodn. vol. 45, pp. 1214–1218, 2011).
- [3] J. Požela, K. Požela and V. Jucienė, "Electron mobility and electron scattering by polar optical phonons in heterostructure quantum wells, "*Semiconductors*," vol. 34, pp. 1011-1015, 2000 (Engl. Transl. of *Fiz. Tekh. Poluprovodn*. vol. 34, pp. 1053–1057, 2000).
- [4] A. Dmitriev, V. Kachorovski, M. S. Shur and M. Stroscio, "Electron drift velocity of two-dimensional electron gas in compound semiconductors," *Intern. J. High Speed Electronics and Systems*, vol. 10, pp. 103110, 2000.
- [5] J. Požela, *Physics of High-Speed Transistors*, New York and London, Plenum Press, 1993.
- [6] M. Shur, GaAs Devices and Circuits, New York and London, Plenum Press, 1986.
- [7] K. Požela, A. Šilėnas, J. Požela, V. Jucienė, G. B. Galiev, I. S. Vasil'evskii and E. A. Klimov, "Effects of phonon confinement on highelectric field electron transport in an InGaAs/InAlAs quantum well with an inserted InAs barrier," *Appl. Phys. A*, vol. 109, pp. 233–237, 2012.
- [8] D. R. Anderson, N. A. Zakhleniuk, M. Babiker, B. K. Ridley and C. R. Bennet, "Polar-optical phonon-limited transport in degenerate GaN-based quantum wells," *Phys. Rev. B*, vol. 63, pp. 245313-1–7, 2001.
- [9] M. Ramonas, A. Matulionis, J. Liberis, L. Eastman, X. Chen and Y. -J. Sun, "Hot-phonon effect on power dissipation in a biased Al_xGa_{1-x}N/AlN/GaN channel," *Phys. Rev. B*, vol. 71, pp. 075324-1–8, 2005.
- [10] J. Wang, J. -P. Leburton and J. Pozela, "Phonon dispersion and electron-polar optical phonon interaction in coupled quantum-well structures in the modified image charge ansatz approach," J. Appl. Phys., vol. 81, pp. 3468-3473, 1997.
- [11] J. Požela, K. Požela and V. Jucienė, "Scattering of electrons by confined interface polar optical phonons in a double-barrier heterostructure, "Semiconductors, vol. 41, pp. 1074–1079, 2007 (Engl. Transl. of Fiz. Tekh. Poluprovodn. vol. 41, pp. 1093–1098, 2007).
- [12] Yu. Požela, K. Požela, V Jucienė, S. Balakauskas, V. P. Evtikhiev, A. S. Shkolnik, Yu. Storasta and A. Mekys, "An increase in the electrom mobility in the two-barrier AlGaAs/GaAs/AlGaAs heterostructure as a result of introduction of thin InAs barriers for polar optical phonons into the GaAs quantum well," *Semiconductors*, vol. 41, pp. 1439–1444, 2007 (Engl. Transl. of *Fiz. Tekh. Poluprovodn.*, vol. 41, pp. 1460–1465, 2007).
- [13] J. K. Požela and V. G. Mokerov, "A large enhancement of maximum drift velocity of electrons in the channel of a field-effect heterotransistor," *Semiconductors*, vol. 40, pp. 357–361, 2006 (Engl. Transl. of *Fiz. Tekh.Poluprovodn.*, vol. 40, pp. 362–366, 2006).
- [14] J. Požela, K. Požela, A. Sužiedėlis, V, Jucienė and V. Petkun, "Enhanced electron saturated drift velocity in AlGaAs/GaAs/AlGaAs heterostructures," Acta Phys. Polon. A, vol. 113, pp. 989–992, 2008.
- [15] V. G. Mokerov, J. Pozela, K. Pozela and V. Juciene, "Giant increase of electron maximum drift velocity in a MODFET channel," in: Nonequilibrium Carrier Dynamics in Semiconductors / Series: Springer Proc. in Physics, 2006, vol. 110, pp. 245–248.

- [16] J. Požela, K. Požela, A. Sužiedėlis and Č. Paškevič, "Saturated electron drift velocity at high electric fields in AlGaAs/GaAs/AlGaAs heterostructures," *Lithuanian J. Phys.*, vol. 50, pp. 397–402, 2010.
- [17] J. Požela, K. Požela, R. Raguotis and V. Jucienė, "Drift velocity of electrons in quantum wells of selectively doped In_{0.5}Ga_{0.5}As/Al_xIn₁. _xAs and In_{0.2}Ga_{0.8}As/Al_xGa_{1-x}As heterostructures in high electric fields," *Semiconductors*, vol. 45, pp. 761–765, 2011 (Engl. Transl. of *Fiz. Tekh.Poluprovodn.*, vol. 45, pp.778–782, 2011).
- [18] J. Požela, K. Požela, R. Raguotis and V. Jucienė, "Transport of electrons in GaAs quantum well in high electric fields," *Semiconductors*, vol. 43, pp.1177–1181, 2009 (Engl. Transl. of *Fiz. Tekh.Poluprovodn.*, vol. 43, pp. 1217–1221, 2009).
- [19] J. Požela, K. Požela and V. Jucienė, "Electron scattering by interface polar optical phonons in double barrier heterostructures," *Lithuanian J. Phys*, vol. 47, pp. 41–49, 2007.
- [20] G. B. Galiev, I. S. Vasil'evskii, E. A. Klimov, V. G. Mokerov and A. A. Cherechukin, "Influence of the spacer layer growth temperature on 2D electron gas mobility in pseudomorphic high electron mobility transistor structures," *Semiconductors*, vol. 40, pp. 1445–1449, 2006 (Engl. Transl. of *Fiz. Tekh.Poluprovodn.*, vol. 40, pp. 1479–1483, 2006).
- [21] J. Požela, K. Požela, A. Shkolnik, A. Sužiedėlis, V. Jucienė, S. Mikhrin and V. Mikhrin, "High-field electron mobility in InGaAs quantum wells," *Phys. Status Solidi C*, vol. 6, pp. 2713–2715, 2009.
- [22] I. S. Vasil'evskii, G. B. Galiev, Yu. A. Matveev, E. A. Klimov, J. Požela, K. Požela, A. Sužiedėlis, Č. Paškevič and V. Jucienė, "Electron transport in an In_{0.52}Al_{0.48}As/In_{0.53}Ga_{0.47}As/In_{0.52}Al_{0.48}As quantum well with a δ-Si doped barrier in high electric fields," *Semiconductors*, vol. 44, pp. 898–903, 2010 (Engl. Transl. of *Fiz. Tekh.Poluprovodn.*, vol. 44, pp. 928–933, 2010.
- [23] V. A. Kulbachinskii, I. S. Vasil'evskii, R. A. Lunin, G. Galistu, A. de Visser, G. B. Galiev, S. S. Shirokov and V. G. Mokerov, "Electron transport and optical properties of shallow GaAs/InGaAs/GaAs quantum wells with a thin central AlAs barrier," *Semicond. Sci. Technol.*, vol. 22, pp. 222–228, 2007.
- [24] V. G. Mokerov, I. S. Vasil'evskii, G. B. Galiev, J. Požela, K. Požela, A. Sužiedėlis, V. Jucienė and Č. Paškevič, "Drift velocity of electrons in quantum wells in high electric fields," *Semiconductors*, vol. 43, pp. 458–462, 2009 (Engl. Transl. of *Fiz. Tekh.Poluprovodn.*, vol. 43, pp. 478–482, 2009).
- [25] X. Wallart, B. Pinsard and F. Mollot, "High-mobility InGaAs/InAlAs pseudomorphic heterostructures on InP (001)," J. Appl. Phys., vol. 97, p. 053706, 2005.
- [26] V. Drouot, M. Gendry, C. Santinelli, P. Victorovitch, G. Hollinger, S. Elleuch and J.-L. Pelouard, "High electron mobility in pseudomorphic modulation-doped In_{0.75}Ga_{0.25}As/InAlAs heterostructures achieved with growth interruptions," J. Appl. Phys., vol. 77, pp. 1810–1812, 1995.
- [27] M. Tacano, Y. Sugiyama, Y. Takeuchi and Y. Ueno, "Very high electron mobility In_{0.8}Ga_{0.2}Asheterostructure grown by molecular beam epitaxy," J. Electron. Mater., vol. 20, pp. 1081–1085, 1991.
- [28] P. P. Malcev, Yu. V. Fedorov, G. B. Galiev, A. S. Bugaev, A. P. Senichkin and D. L. Gnatiuk, "Developments in microwave nanoelectronics," *Nano- i Mikrosistemnaya tekhnika*, no. 11, p. 14, 2010 (In Russian).



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